

FIG. 1 (PRIOR ART)

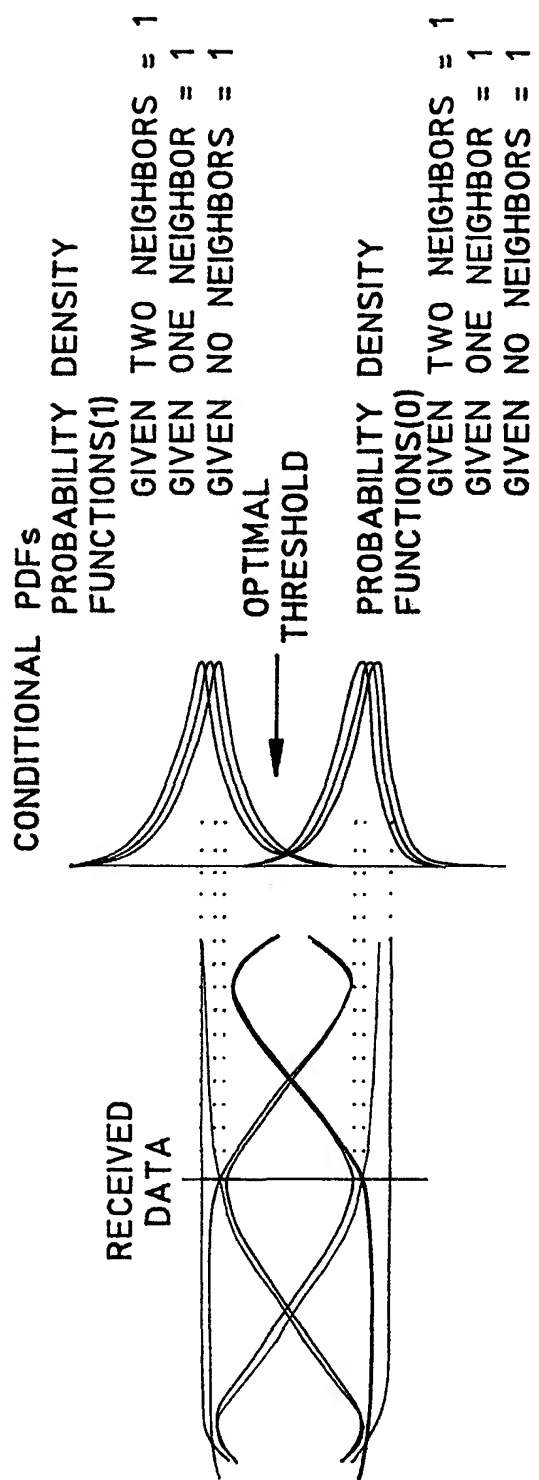


FIG. 2 (PRIOR ART)

300

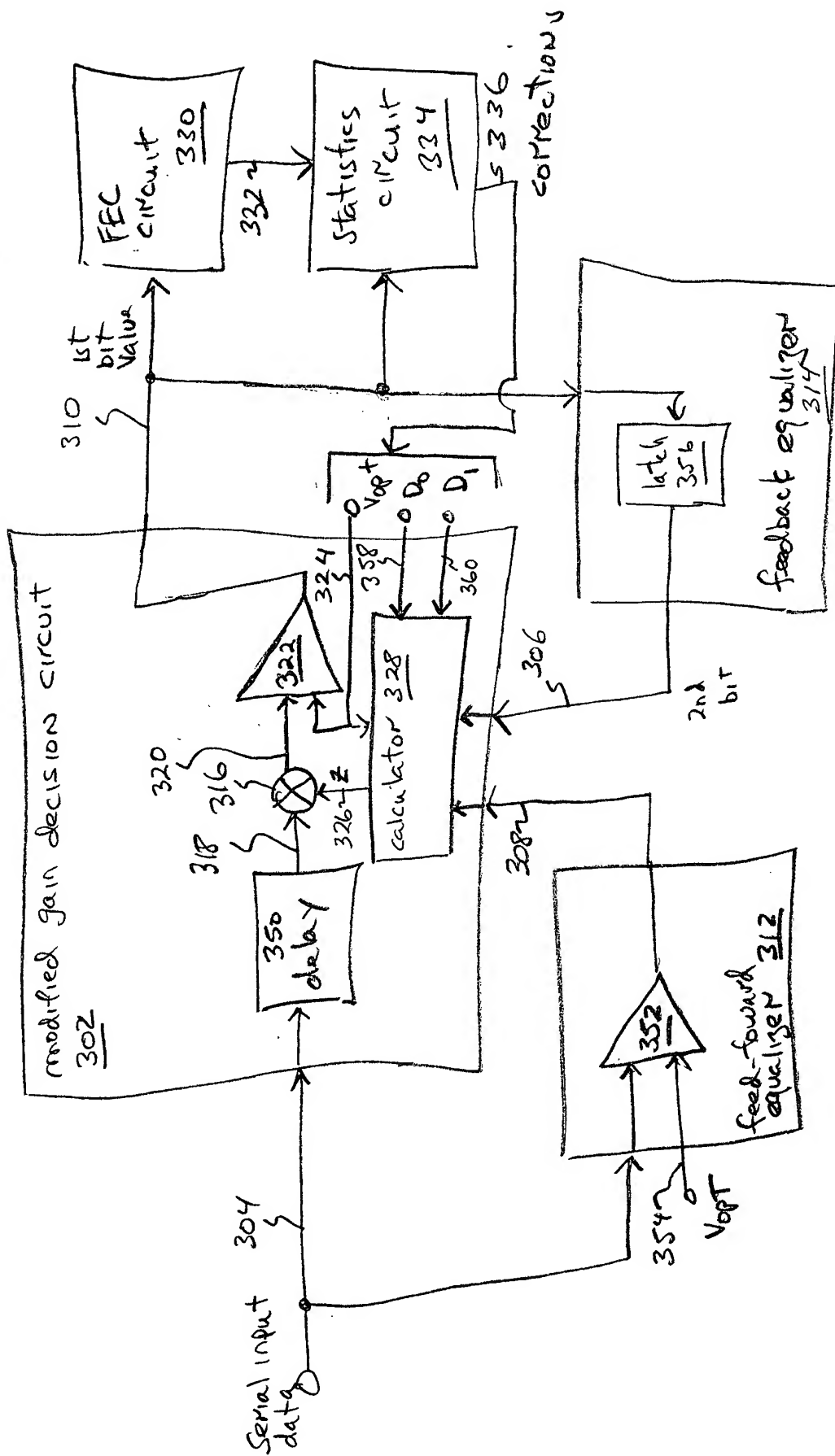


Fig. 3

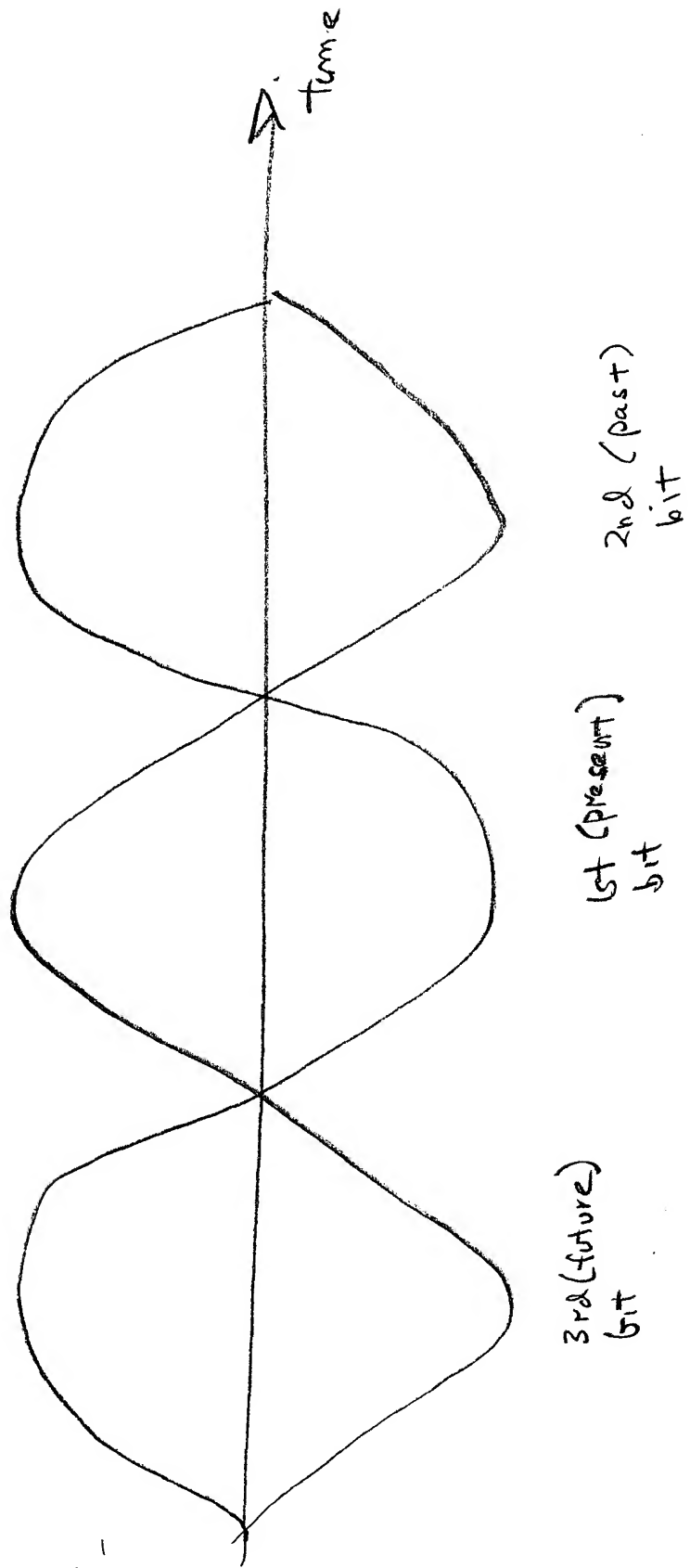


Fig. 4

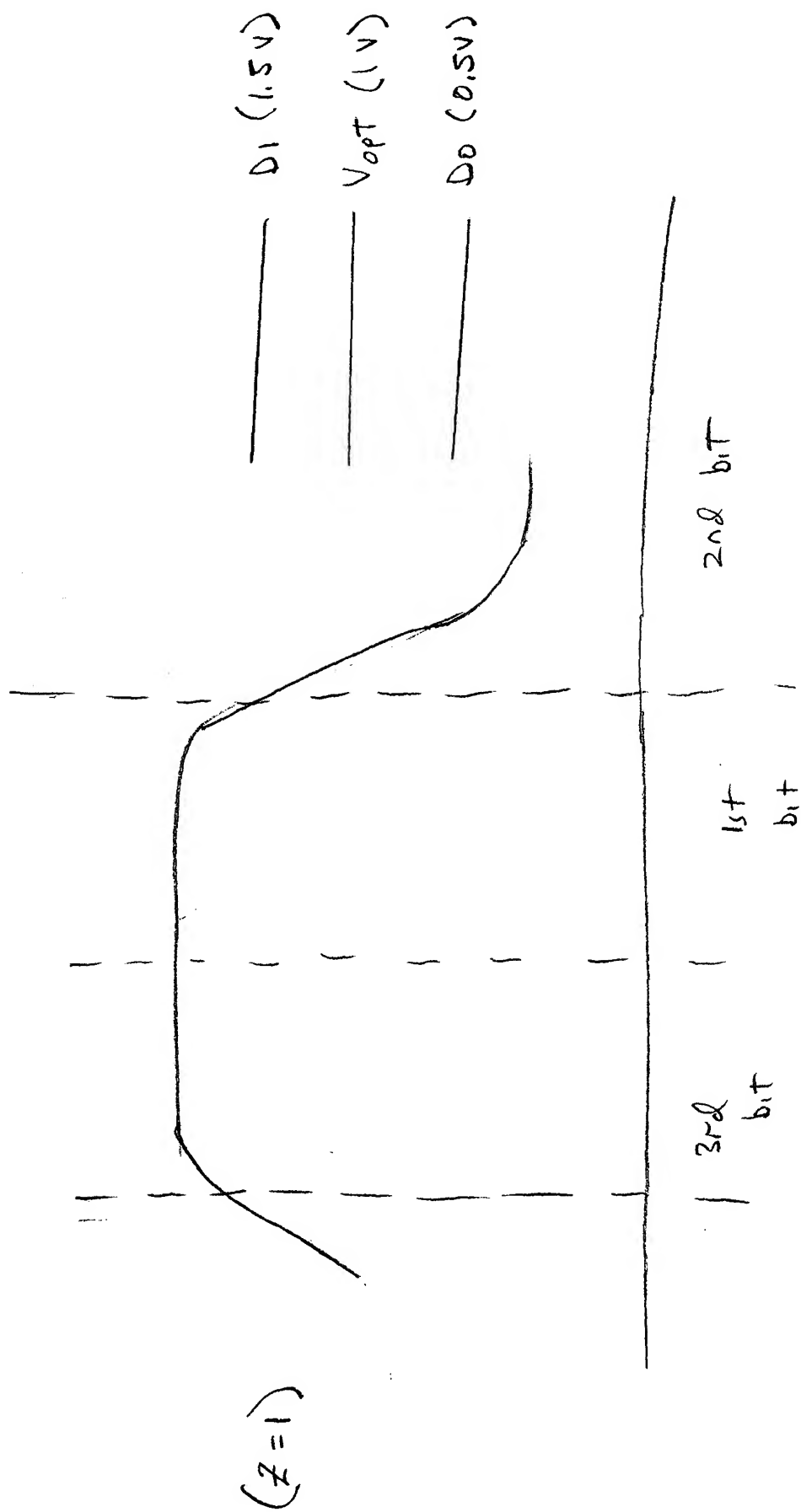


Fig. 5

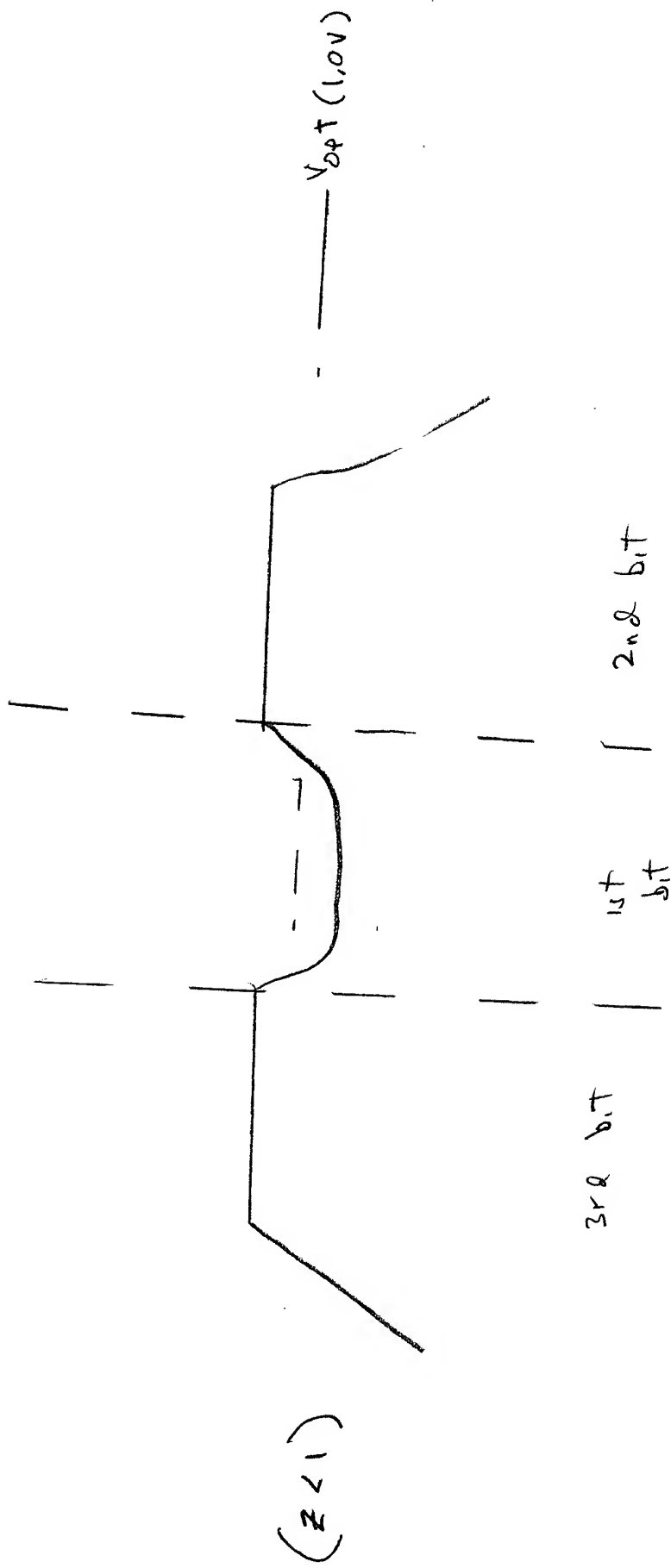


Fig. 6

INPUT LEVEL

Di	<u>DEFINITE "1"</u> "0" IF BOTH PAST & FUTURE BIT VALUES = "1" "1" OTHERWISE
Vopt	<u> </u> "1" IF BOTH PAST & FUTURE BIT VALUES = "0" "0" OTHERWISE
Do	<u>DEFINITE "0"</u>

Fig. 7

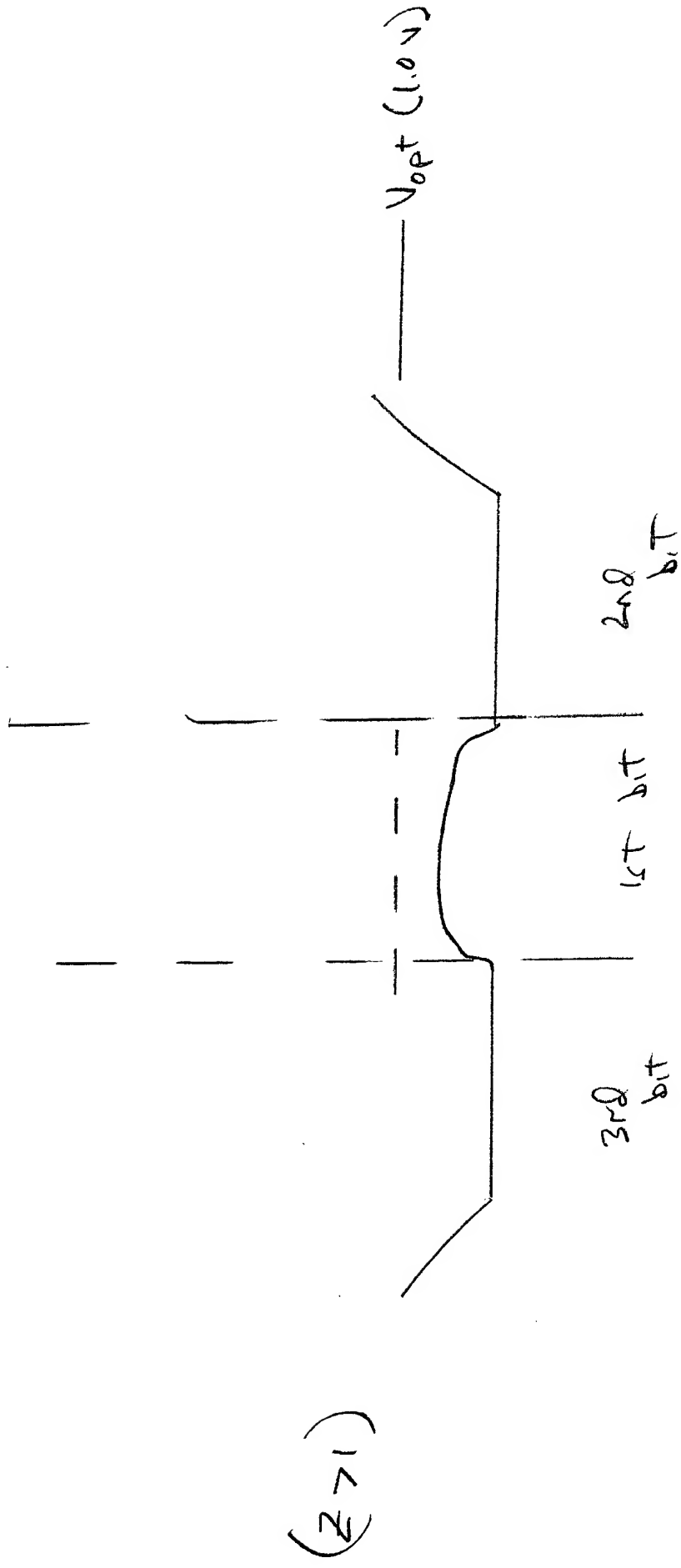


Fig. 8

amplitude modifier	2nd bit value	3rd bit value
low	1	1
high	0	0
unity	1	0
unity	0	1

Fig. 9

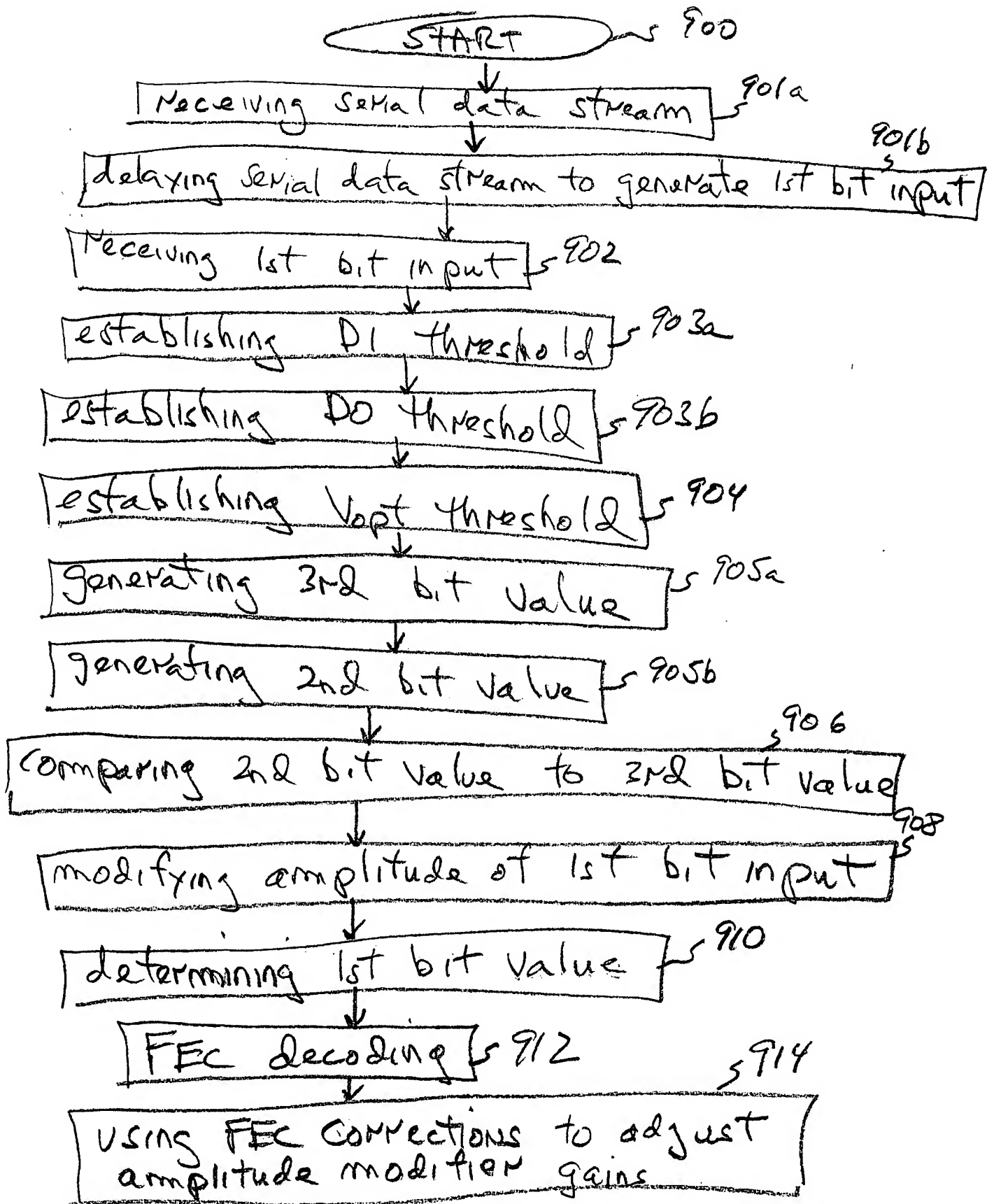


Fig. 10